

Electrostatic Discharged Protection Devices (ESD) Data Sheet

Description

The SBD8C05L01 is designed to replace multilayer varistors (MLVs) in portable applications such as cell phones, notebook computer, and PDAs. It offer superior electrical characteristics such as lower clamping voltage and no device degradation when compared to MLVs. It is designed to protect sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD), lightning, electrical fast transients (EFT), and cable discharge events (CDE).

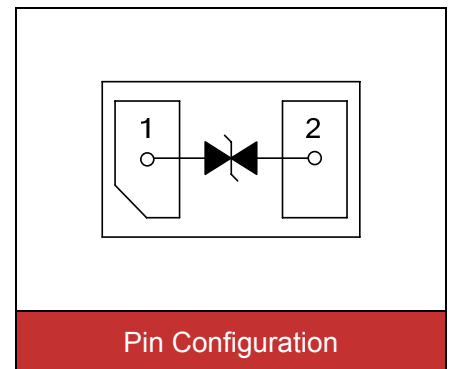


Contact : ±8kV
Air : ±15kV



Features

- IEC61000-4-2 ESD 15KV Air, 8KV contact compliance
- SOD882 surface mount package
- Protects bi-directional line
- Working voltage: 5V
- Low leakage current
- Low operating and clamping voltages
- Solid-state silicon avalanche technology
- Lead Free/RoHS compliant
- Solder reflow temperature: Pure Tin-Sn, 260~270°C
- Flammability rating UL 94V-0
- Meets MSL level 1, per J-STD-020
- Marking: QB



Applications

- | | |
|---|---|
| <ul style="list-style-type: none"> ● Cellular handsets & Accessories ● Cordless phones ● Personal digital assistants (PDAs) ● Notebooks & Handhelds | <ul style="list-style-type: none"> ● Portable instrumentation ● Digital cameras ● Peripherals ● MP3 players |
|---|---|

Maximum Ratings

Rating	Symbol	Value	Unit
ESD voltage (Contact discharge)	V_{ESD}	±8	kV
ESD voltage (Air discharge)		±15	
Storage & operating temperature range	T_{STG}, T_J	-55~+150	°C

Electrical Characteristics ($T_J=25^{\circ}\text{C}$)

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Reverse stand-off voltage	V_{RWM}				5	V
Reverse breakdown voltage	V_{BR}	$I_{BR}=1\text{mA}$	5.6		8	V
Reverse leakage current	I_R	$V_R=5\text{V}$			5	μA
Clamping voltage ($t_p=8/20\mu\text{s}$)	V_C	$I_{PP}=1\text{A}$			8	V
Clamping voltage ($t_p=8/20\mu\text{s}$)	V_C	$I_{PP}=5\text{A}$			12	V
Off state junction capacitance	C_J	0Vdc, f=1MHz		30		pF

Typical Characteristics Curves

Figure 1. Power Derating Curve

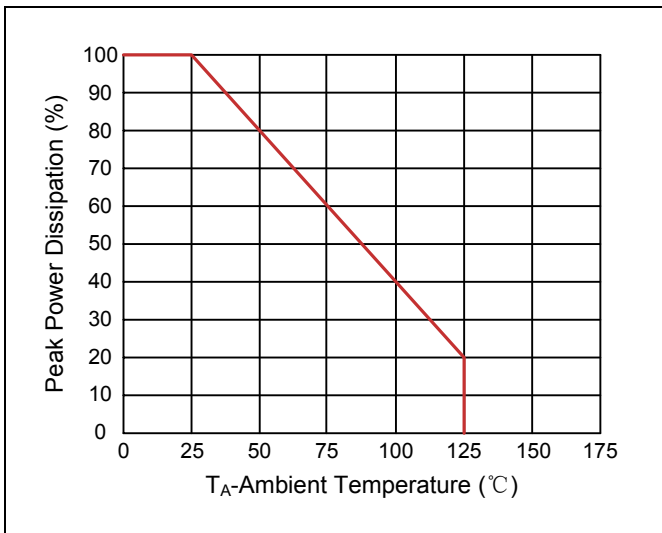


Figure 2. Pulse Waveforms

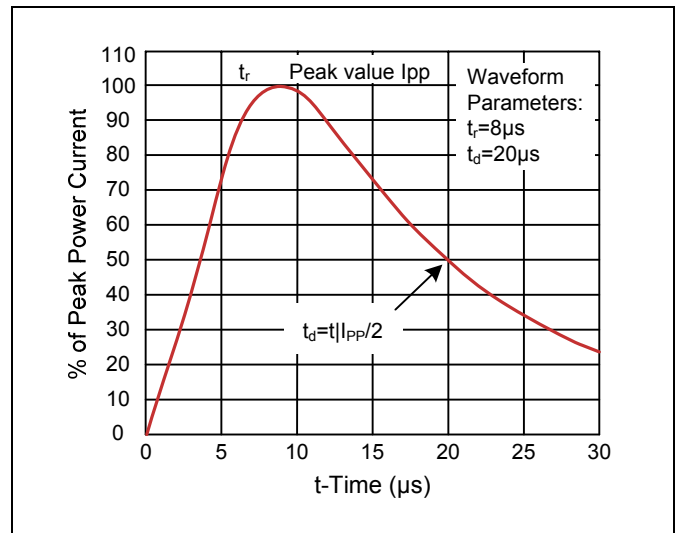


Figure 3. Clamping Voltage vs. Peak Pulse Current

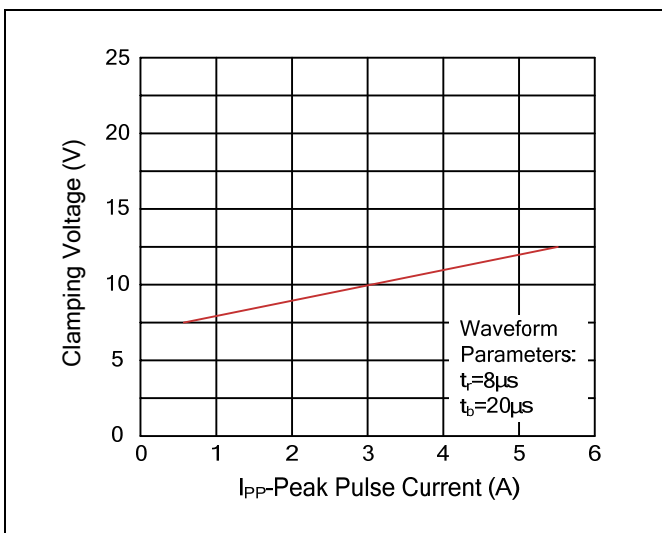
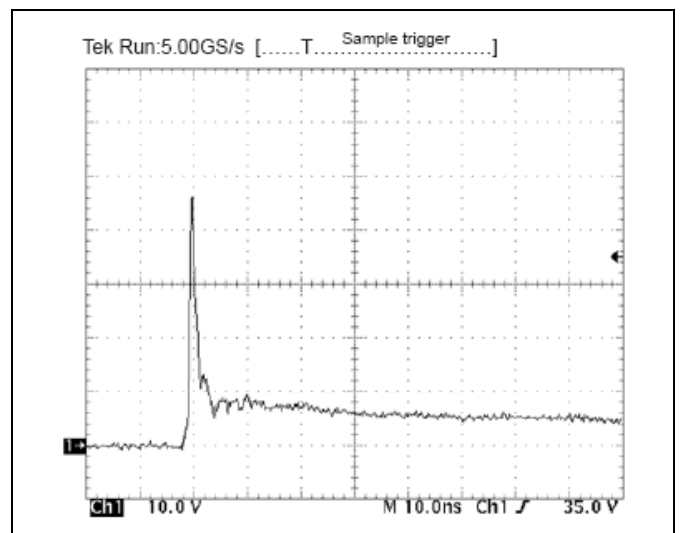
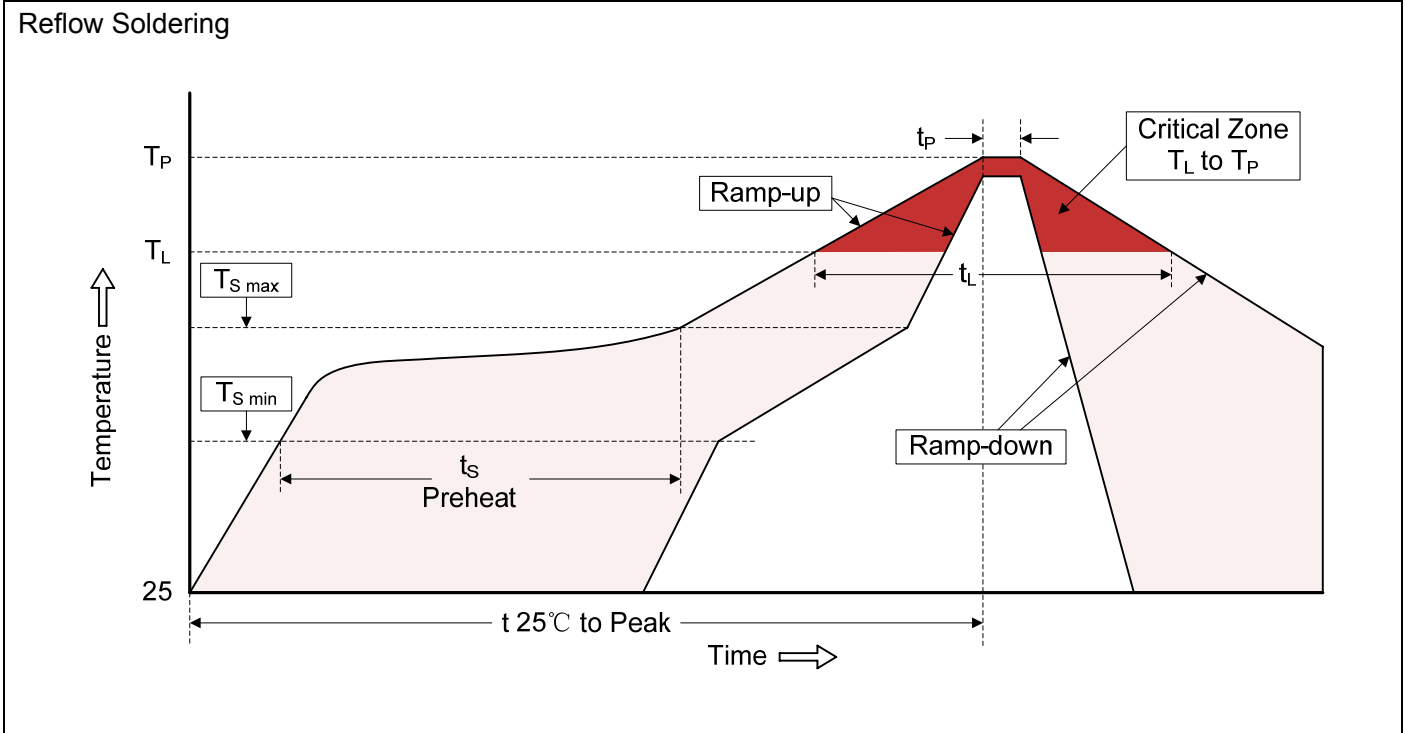


Figure 4. ESD Clamping(8kV Contact IEC61000-4-2)



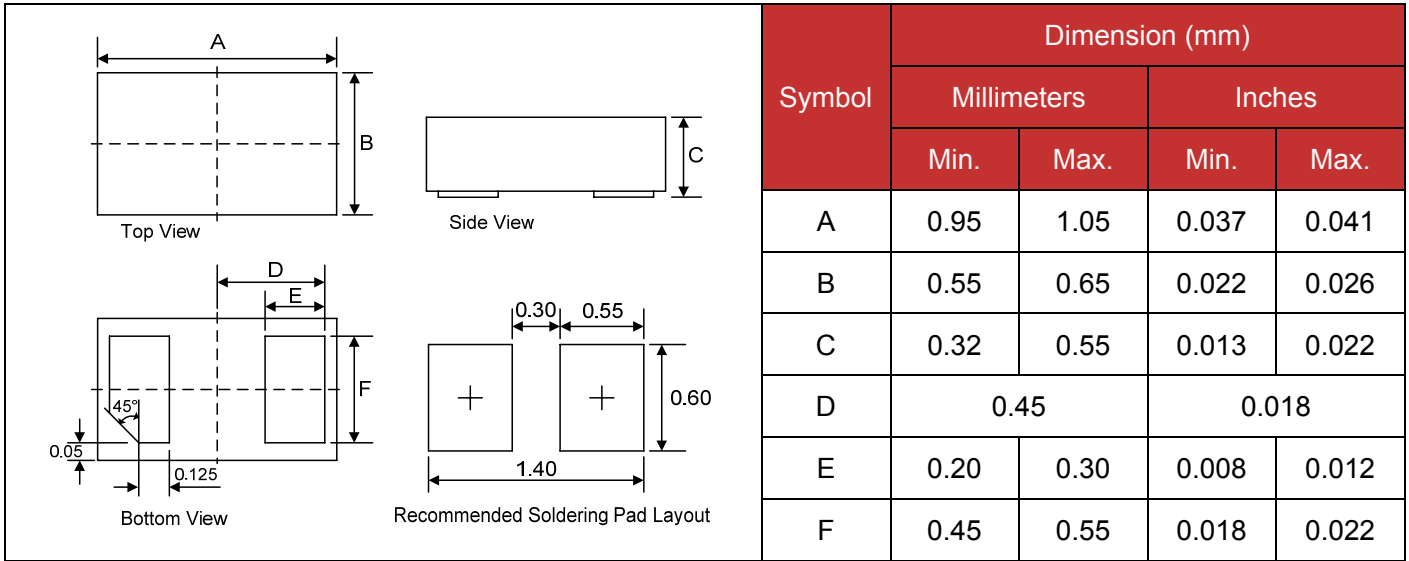
Recommended Soldering Conditions



Recommended Conditions

Profile Feature	Pb-Free Assembly
Average ramp-up rate (T_L to T_P)	3°C/second max.
Preheat -Temperature Min ($T_{S\ min}$) -Temperature Max ($T_{S\ max}$) -Time (min to max) (t_s)	150°C 200°C 60-180 seconds
$T_{S\ max}$ to T_L -Ramp-up Rate	3°C/second max.
Time maintained above: -Temperature (T_L) -Time (t_L)	217°C 60-150 seconds
Peak Temperature (T_P)	260°C
Time within 5°C of actual Peak Temperature (t_p)	20-40 seconds
Ramp-down Rate	6°C/second max.
Time 25°C to Peak Temperature	8 minutes max.

Dimensions (SOD882)



Packaging

